

What is claimed is:

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1. A receiving terminal for CDMA system comprising at least a finger circuit for taking a correlation of a received signal from a radio circuit connected to an antenna and known signal and feeding out the correlated received signal, and a lake circuit for combining a plurality of outputs from the finger circuits and executing level measurement, wherein:

the lake circuit includes a level judgment circuit for executing electric field judgment according to the correlated received signal from the finger circuit and a predetermined threshold level, the operation of a predetermined circuit being suspended according to the result of the level judgment.

2. The receiving terminal for CDMA system according to claim 1, wherein the operation control clock supply to the finger circuit is suspended for power consumption reduction according to the result of the level judgment in the level judging circuit.

3. The receiving terminal for CDMA system according to claim 1, wherein the operation control clock supply to a timing circuit in the finger circuit is suspended according to the result of level judgment in the level judging circuit.

4. The receiving terminal for CDMA system according

to claim 1, wherein the operation control clock supply is suspended after the lapse of a predetermined period of time.

5. The receiving terminal for CDMA system according to claim 1, wherein the operation control clock supply to the finger circuit or to the timing circuit therein is resumed after the lapse of predetermined period of time.

6. The receiving terminal for CDMA system according to claim 1, wherein the threshold value is preset in a memory.

7. The receiving terminal for CDMA system according to 4, wherein the memory is an E2PROM, and threshold data therefrom is supplied under CPU control to the lake circuit.

8. The receiving terminal for CDMA system according to claim 1, wherein the finger circuit takes correlation of output signal data fed out from the radio circuit and known signal data to each other, demodulates the correlated data to symbol unit data, and feeds out the demodulated data to the lake circuit.

9. The receiving terminal for CDMA system according to claim 1, wherein the level measurement is executed by computing the power level in a pilot symbol part in one

frame for each slot and adding together the results of the computation for one frame.

10. The receiving terminal for CDMA system according to claim 1, wherein the finger circuit includes a plurality of finger circuit elements, which each obtains the difference of the maximum level and a pertinent level among the electric field levels in them and compare the difference and the threshold value with each other.

11. A receiving terminal for CDMA system for receiving received signals from a plurality of signal propagation channels, wherein:

the electric field level of the received signal from each signal propagation channel is judged, and the operation control clock supply to a circuit system receiving signal from a low electric field level signal propagation channel is suspended for a predetermined period of time for power consumption reduction.

12. A receiver for CDMA system comprising at least a finger circuit for taking a correlation of a received signal from a radio circuit connected to an antenna and known signal and feeding out the correlated received signal, and a lake circuit for combining a plurality of outputs from the finger circuits and executing level measurement, wherein:

the lake circuit includes a level judgment circuit

for executing electric field judgment according to the correlated received signal from the finger circuit and a predetermined threshold level, the operation of a predetermined circuit being suspended according to the result of the level judgment.

13. The receiver for CDMA system according to claim 12, wherein the operation control clock supply to the finger circuit is suspended for power consumption reduction according to the result of the level judgment in the level judging circuit.

14. The receiver for CDMA system according to claim 12, wherein the operation control clock supply to a timing circuit in the finger circuit is suspended according to the result of level judgment in the level judging circuit.

15. The receiver for CDMA system according to claim 12, wherein the operation control clock supply is suspended after the lapse of a predetermined period of time.

16. The receiver for CDMA system according to claim 12, wherein the operation control clock supply to the finger circuit or to the timing circuit therein is resumed after the lapse of predetermined period of time.

17. The receiver for CDMA system according to claim

12, wherein the threshold value is preset in a memory.

18. The receiver for CDMA system according to 15, wherein the memory is an E2PROM, and threshold data therefrom is supplied under CPU control to the lake circuit.

19. The receiver for CDMA system according to claim 12, wherein the finger circuit takes correlation of output signal data fed out from the radio circuit and known signal data to each other, demodulates the correlated data to symbol unit data, and feeds out the demodulated data to the lake circuit.

20. The receiver for CDMA system according to claim 12, wherein the level measurement is executed by computing the power level in a pilot symbol part in one frame for each slot and adding together the results of the computation for one frame.

21. The receiver for CDMA system according to claim 12, wherein the finger circuit includes a plurality of finger circuit elements, which each obtains the difference of the maximum level and a pertinent level among the electric field levels in them and compare the difference and the threshold value with each other.

22. A receiver for CDMA system for receiving

received signals from a plurality of signal propagation channels, wherein:

the electric field level of the received signal from each signal propagation channel is judged, and the operation control clock supply to a circuit system receiving signal from a low electric field level signal propagation channel is suspended for a predetermined period of time.

23. A receiving method for CDMA system with step for taking a correlation of a received signal and known signal and combining a plurality of correlated signals for level measurement, the method further comprising:

executing electric field judgment according to the correlated received signal and a predetermined threshold level, and suspending an operation of a predetermined circuit according to the result of the level judgment.

24. A receiving method for CDMA system for receiving received signals from a plurality of signal propagation channels including steps of:

judging the electric field level of the received signal from each signal propagation channel; and

suspending the operation control clock supply to a circuit receiving signal from a low electric field level signal propagation channel for a predetermined period of time.